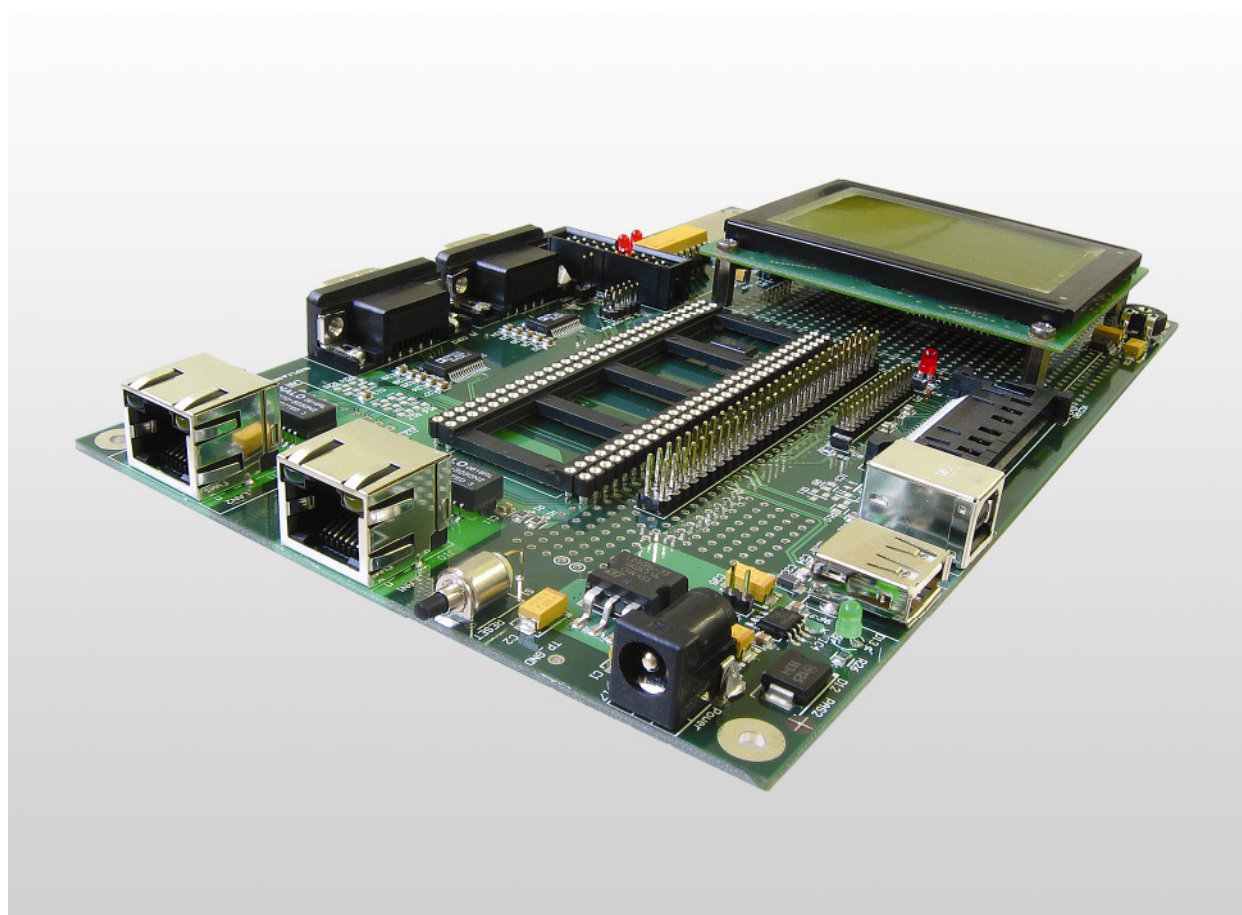


DNP/EVA11

Board Revision 1.0

Hardware Reference



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1 INTRODUCTION

This document describes the hardware components of the DNP/EVA11. For further information about the individual components of this product you may follow the links from our website at <http://www.dilnetpc.com>. Our website contains a lot of technical information, which will be updated in regular periods.

1.1 Safety Guidelines

Please read the following safety guidelines carefully! In case of property or personal damage by not paying attention to this document and/or by incorrect handling, we do not assume liability. In such cases any warranty claim expires.



ATTENTION: Observe precautions for handling – electrostatic sensitive device!

- Discharge yourself before you work with the device, e.g. by touching a heater of metal, to avoid damages.
- Stay grounded while working with the device to avoid damage through electrostatic discharge.

1.2 Conventions

Convention	Usage
bold	Important terms
<i>italic</i>	Filenames, user inputs and command lines
monospace	Pathnames, internet addresses and program code

Table 1: Conventions used in this Document

1.3 Block Diagram

The QIL-128 socket (J1) forms the center of this figure. It connects the ADNP/9200 with the DNP/EVA11 functions.

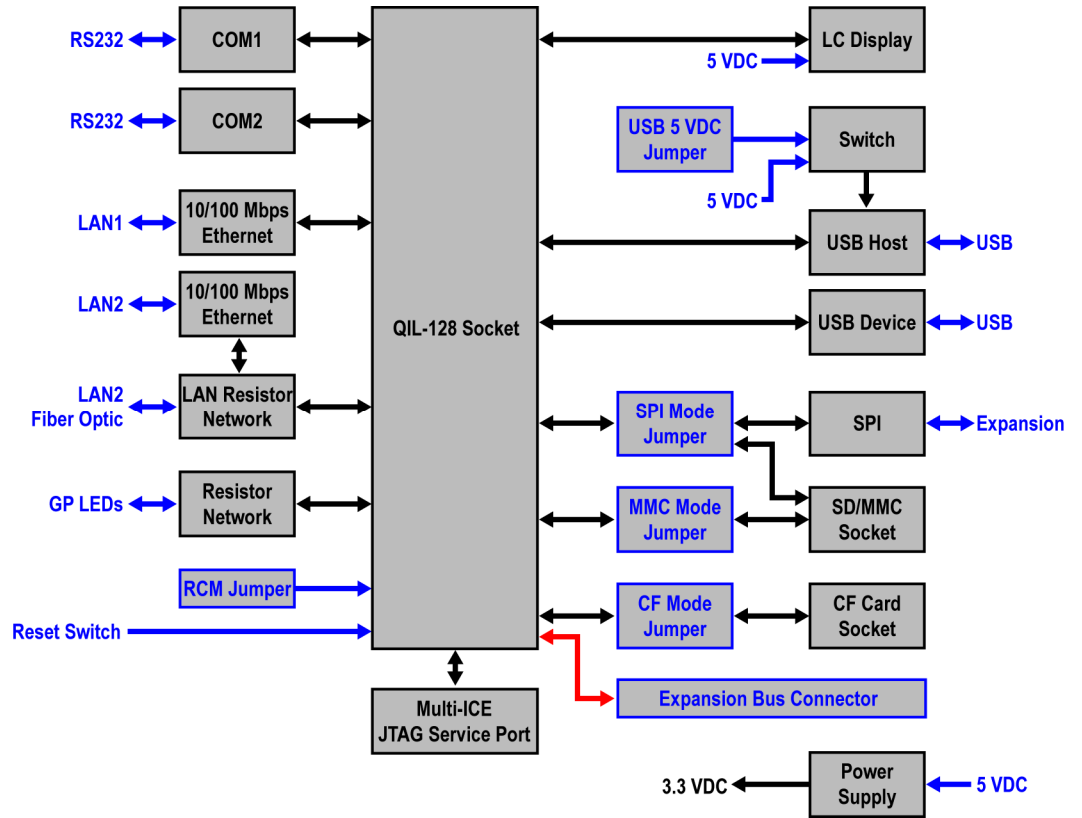


Figure 1: Block diagram of DNP/EVA11

1.4 Features and Technical Data

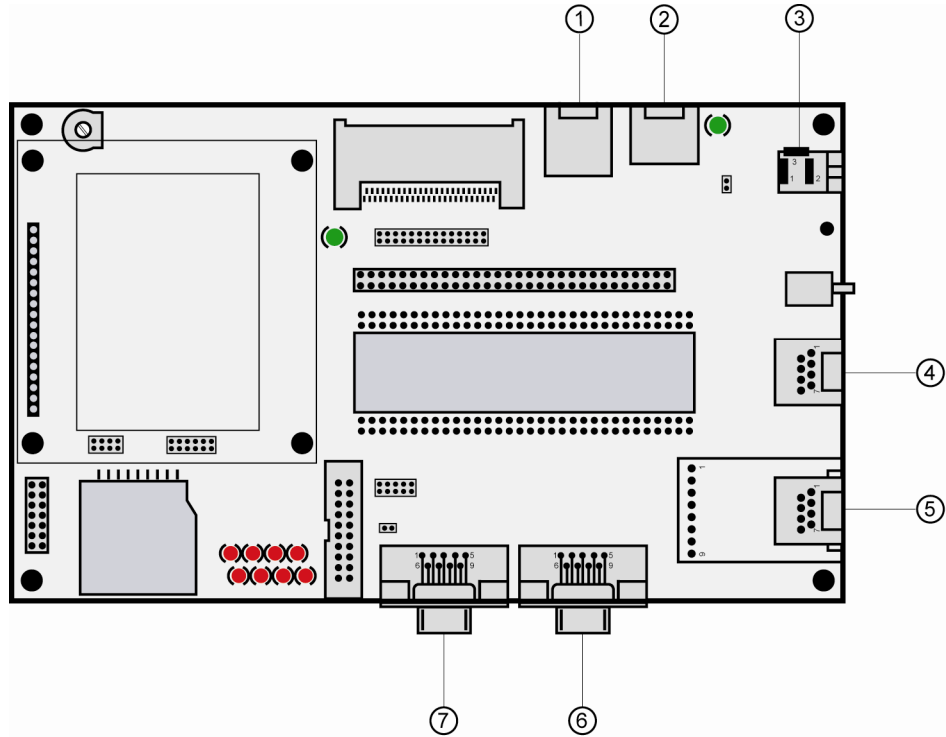
	Evaluation Board DNP/EVA11
Socket	QIL-128
Serial Ports	2 x RS232
Ethernet 1	10/100 Mbps (10BASE-T)
Ethernet 2*	10/100 Mbps (10BASE-T) or 100Mbps (100BASE-FX)
SPI Connector	✓
Multi-ICE Connector	✓
JTAG Port	✓
SD/MMC Socket	✓
CF Socket	✓
Expansion Bus	✓
USB Host Port	✓ (with 5 VDC power supply output)
USB Device Port	✓
LCD	✓ (128 x 64 dots)
LEDs	1 x power, 1 x IDE, 8 x general purpose (PIO port A)
Power	5 Volt DC input connector
Size	200 mm x 120 mm
DIL/NetPC	ADNP/9200
RoHS	✓

Table 2: Features of DNP/EVA11

* = The order code for the DNP/EVA11 with two 10BASE-T connectors is **DNP/EVA11-TX**. The order code for the DNP/EVA11 with one 10BASE-T and one 100BASE-FX connector is **DNP/EVA11-FX**.

2 BOARD LAYOUT

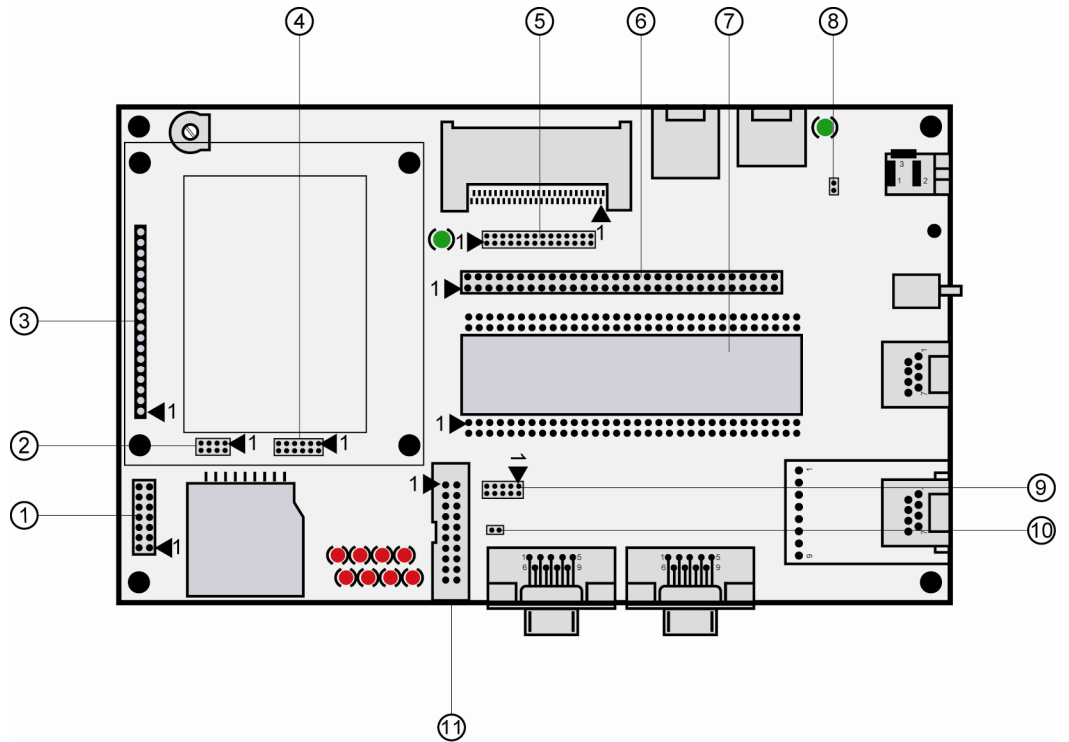
2.1 Overview Interfaces



- ① J8 - USB host port
- ② J9 - USB device port
- ③ J17 - Power connector
- ④ J10 - 10/100 Mbps Ethernet connector 1
- ⑤ J11 - 10/100 Mbps Ethernet connector 2 or 100BASE-FX SC cable connector 2
- ⑥ J5 - COM2 (RS232)
- ⑦ J4 - COM1 (RS232)

Figure 2: Interfaces DNP/EVA11

2.2 Overview Jumpers and Connectors



- | | |
|-------------------------|-----------------------------|
| ① J14 - SPI connector | ⑦ J1 - QIL-128 socket |
| ② JP4 - SPI mode jumper | ⑧ JP5 - USB VCC jumper |
| ③ J3 - LCD connector | ⑨ J15 - JTAG service port |
| ④ JP3 - MMC mode jumper | ⑩ JP1 - RCM jumper |
| ⑤ JP2 - CF mode jumper | ⑪ J16 - Multi-ICE connector |
| ⑥ J2 - Expansion bus | |

Figure 3: Jumpers and connectors DNP/EVA11

2.3 Overview LEDs, Buttons, etc.

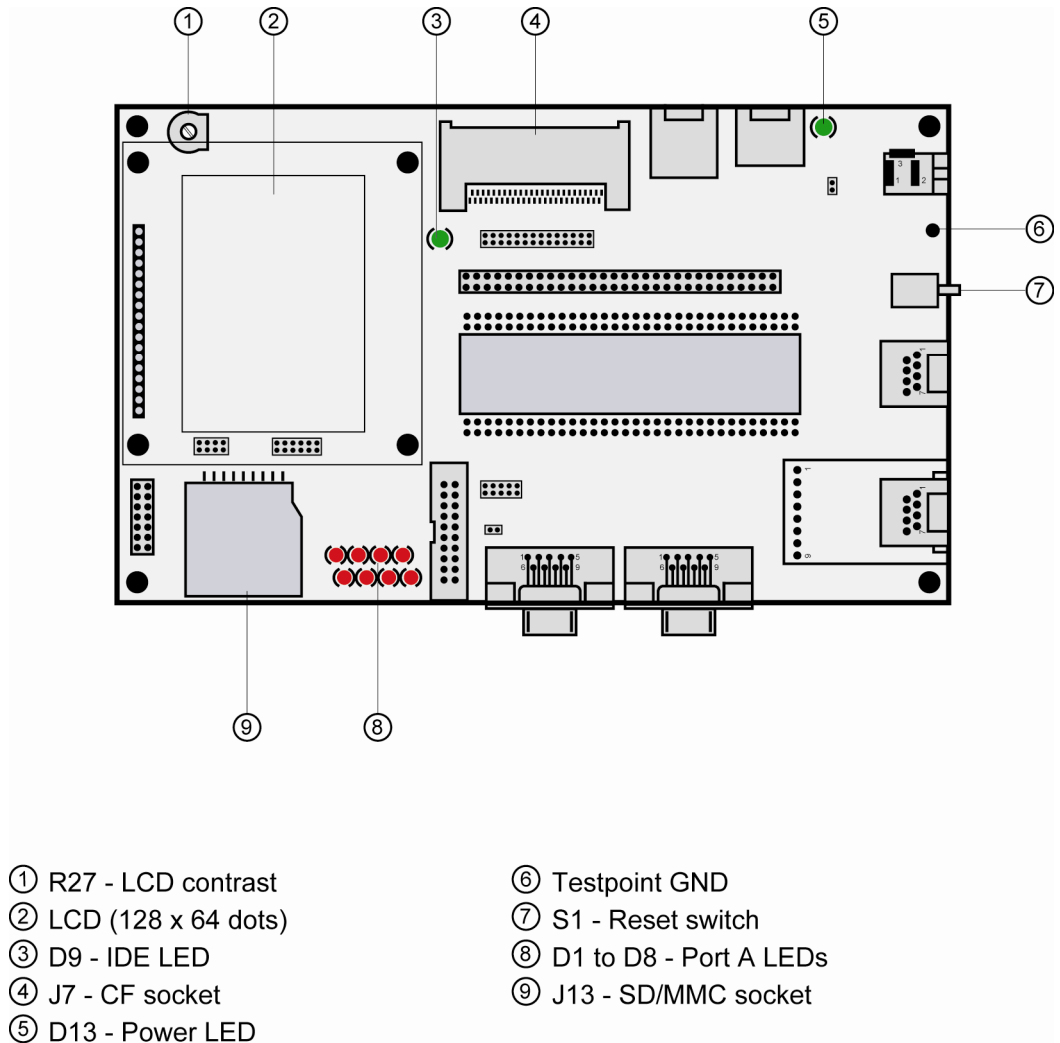


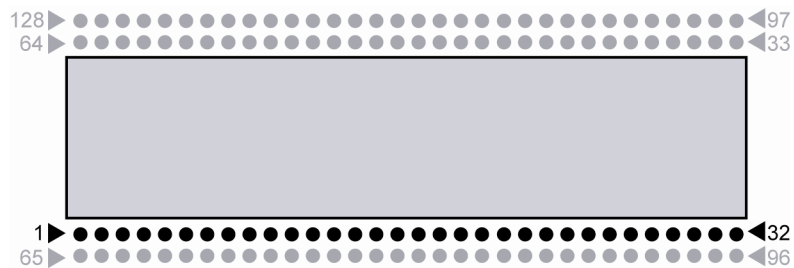
Figure 4: LEDs, buttons, etc. DNP/EVA11

3 PINOUTS

3.1 QIL-128 Socket – J1 (1. Part)

Pin	Name	Group	Function	Used
1	PA0	PIO	Parallel I/O, Port A, Bit 0	✓
2	PA1	PIO	Parallel I/O, Port A, Bit 1	✓
3	PA2	PIO	Parallel I/O, Port A, Bit 2	✓
4	PA3	PIO	Parallel I/O, Port A, Bit 3	✓
5	PA4	PIO	Parallel I/O, Port A, Bit 4	✓
6	PA5	PIO	Parallel I/O, Port A, Bit 5	✓
7	PA6	PIO	Parallel I/O, Port A, Bit 6	✓
8	PA7	PIO	Parallel I/O, Port A, Bit 7	✓
9	PB0	PIO	Parallel I/O, Port B, Bit 0	✓
10	PB1	PIO	Parallel I/O, Port B, Bit 1	✓
11	PB2	PIO	Parallel I/O, Port B, Bit 2	✓
12	PB3	PIO	Parallel I/O, Port B, Bit 3	✓
13	PB4	PIO	Parallel I/O, Port B, Bit 4	✓
14	PB5	PIO	Parallel I/O, Port B, Bit 5	✓
15	PB6	PIO	Parallel I/O, Port B, Bit 6	✓
16	PB7	PIO	Parallel I/O, Port B, Bit 7	✓
17	PC0	PIO	Parallel I/O, Port C, Bit 0	✓
18	PC1	PIO	Parallel I/O, Port C, Bit 1	✓
19	PC2	PIO	Parallel I/O, Port C, Bit 2	✓
20	PC3	PIO	Parallel I/O, Port C, Bit 3	✓
21	RXD1	SIO	COM1 Serial Port, RXD Pin	✓
22	TXD1	SIO	COM1 Serial Port, TXD Pin	✓
23	CTS1	SIO	COM1 Serial Port, CTS Pin	✓
24	RTS1	SIO	COM1 Serial Port, RTS Pin	✓
25	DCD1	SIO	COM1 Serial Port, DCD Pin	✓
26	DSR1	SIO	COM1 Serial Port, DSR Pin	✓
27	DTR1	SIO	COM1 Serial Port, DTR Pin	✓
28	RI1	SIO	COM1 Serial Port, RI Pin	✓
29	RESIN	RESET	RESET Input	✓
30	LAN1.TX+	LAN 1	10/100 Mbps Ethernet Interface 1, TX+ Pin	✓
31	LAN1.TX-	LAN 1	10/100 Mbps Ethernet Interface 1, TX- Pin	✓
32	GND	---	Ground	✓

Table 3: Pinout QIL-128 socket – pin 1 to 32



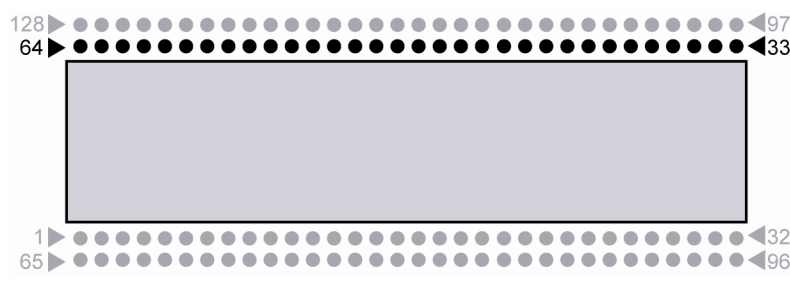
3.2 QIL-128 Socket – J1 (2. Part)

Pin	Name	Group	Function	Used
33	LAN1.RX+	LAN 1	10/100 Mbps Ethernet Interface 1, RX+ Pin	✓
34	LAN1.RX-	LAN 1	10/100 Mbps Ethernet Interface 1, RX- Pin	✓
35	RESOUT	RESET	RESET Output	✓
36	VBAT	PSP*	Real Time Clock Battery Input	---
37	CLKOUT	PSP*	Clock Output	---
38	TXD2	PSP*	COM2 Serial Port, TXD Pin	✓
39	RXD2	PSP*	COM2 Serial Port, RXD Pin	✓
40	HDM	PSP*	USB Host Port -	✓
41	HDP	PSP*	USB Host Port +	✓
42	DDM	PSP*	USB Device Port -	✓
43	DDP	PSP*	USB Device Port +	✓
44	INT1	PSP*	Programmable Interrupt Input 1	---
45	CS4	PSP*	Programmable Chip Select Output 4	---
46	CS3	PSP*	Programmable Chip Select Output 3	---
47	CS2	PSP*	Programmable Chip Select Output 2	✓
48	CS1	PSP*	Programmable Chip Select Output 1	✓
49	IOCHRDY	PSP*	I/O Channel Ready	✓
50	IOR	PSP*	I/O Read Signal, I/O Expansion Bus	✓
51	IOW	PSP*	I/O Write Signal, I/O Expansion Bus	✓
52	SA3	PSP*	Expansion Bus, Address Bit 3	✓
53	SA2	PSP*	Expansion Bus, Address Bit 2	✓
54	SA1	PSP*	Expansion Bus, Address Bit 1	✓
55	SA0	PSP*	Expansion Bus, Address Bit 0	✓
56	SD7	PSP*	Expansion Bus, Data Bit 7	✓
57	SD6	PSP*	Expansion Bus, Data Bit 6	✓
58	SD5	PSP*	Expansion Bus, Data Bit 5	✓
59	SD4	PSP*	Expansion Bus, Data Bit 4	✓
60	SD3	PSP*	Expansion Bus, Data Bit 3	✓
61	SD2	PSP*	Expansion Bus, Data Bit 2	✓
62	SD1	PSP*	Expansion Bus, Data Bit 1	✓
63	SD0	PSP*	Expansion Bus, Data Bit 0	✓
64	VCC	---	3.3 Volt Power Input	✓

* = Product Specific Pins

Table 4: Pinout QIL-128 socket – pin 33 to 64

*** Please note:** Some pins are called "Product Specific Pins (PSP)". Other members of the *DIL/NetPC* family will differ with these pins from the *ADNP/9200*. All other pins will have the same primary functions.



3.3 QIL-128 Socket – J1 (3. Part)

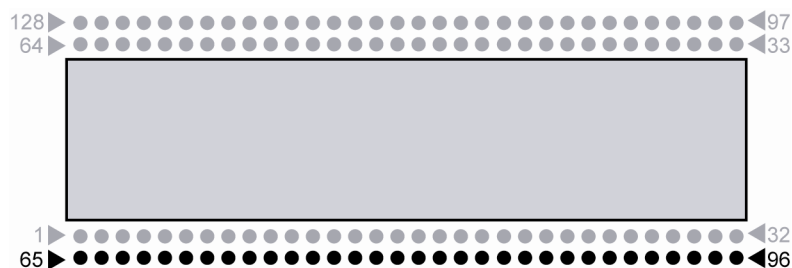
Pin	Name	Group	Function	Used
65	SBHE	PSP*	System Byte High Enable, System Expansion Bus	---
66	IOCS16	PSP*	I/O Chip Select 16, System Expansion Bus	✓
67	Reserved	PSP*	Reserved – Do not use	---
68	Reserved	PSP*	Reserved – Do not use	---
69	Reserved	PSP*	Reserved – Do not use	---
70	Reserved	PSP*	Reserved – Do not use	---
71	Reserved	PSP*	Reserved – Do not use	---
72	Reserved	PSP*	Reserved – Do not use	---
73	RCME	PSP*	Remote Console Mode Enable	✓
74	TDI	PSP*	Test Data In	✓
75	TDO	PSP*	Test Data Out	✓
76	TMS#	PSP*	Test Mode Select	✓
77	TCK	PSP*	Test Clock	✓
78	TRST#	PSP*	Test Reset	✓
79	WDDIS#	PSP*	Watchdog Disable	✓
80	Reserved	PSP*	Reserved – Do not use	---
81	Reserved	PSP*	Reserved – Do not use	---
82	Reserved	PSP*	Reserved – Do not use	---
83	Reserved	PSP*	Reserved – Do not use	---
84	Reserved	PSP*	Reserved – Do not use	---
85	INT6	PSP*	Programmable Interrupt Input 6	✓
86	INT7	PSP*	Programmable Interrupt Input 7	---
87	Reserved	PSP*	Reserved – Do not use	---
88	Reserved	PSP*	Reserved – Do not use	---
89	Reserved	PSP*	Reserved – Do not use	---
90	Reserved	PSP*	Reserved – Do not use	---
91	LAN2.SD**	PSP*	100 Mbps Ethernet Interface 2, SD Pin**	✓
92	LAN2.TX+	PSP*	10/100 Mbps Ethernet Interface 2, TX+ Pin	✓
93	LAN2.TX-	PSP*	10/100 Mbps Ethernet Interface 2, TX- Pin	✓
94	LAN2.RX+	PSP*	10/100 Mbps Ethernet Interface 2, RX+ Pin	✓
95	LAN2.RX-	PSP*	10/100 Mbps Ethernet Interface 2, RX- Pin	✓
96	GND	---	Ground	✓

* = Product Specific Pins

** = This signal is only available with the 100BASE-FX SC cable interface

Table 5: Pinout QIL-128 socket – pin 65 to 96

* **Please note:** Some pins are called "Product Specific Pins (PSP)". Other members of the *DIL/NetPC* family will differ with these pins from the *ADNP/9200*. All other pins will have the same primary functions.



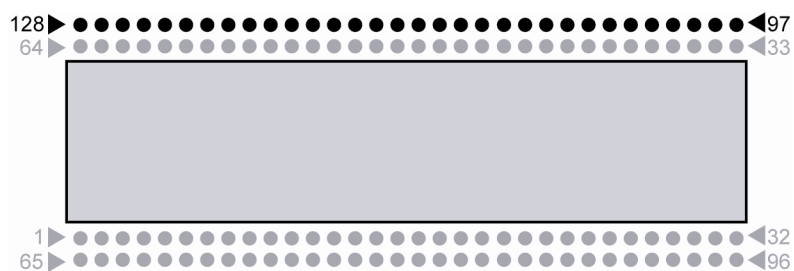
3.4 QIL-128 Socket – J1 (4. Part)

Pin	Name	Group	Function	Used
97	LAN1.LED	PSP*	LAN 1 Interface Activity LED	✓
98	LAN2.LED	PSP*	Reserved – Do not use	✓
99	RSTDRV	PSP*	Reset Output, Expansion Bus	---
100	SA23	PSP*	Expansion Bus, Address Bit 23	---
101	SA22	PSP*	Expansion Bus, Address Bit 22	---
102	SA21	PSP*	Expansion Bus, Address Bit 21	---
103	SA20	PSP*	Expansion Bus, Address Bit 20	---
104	SA19	PSP*	Expansion Bus, Address Bit 19	---
105	SA18	PSP*	Expansion Bus, Address Bit 18	---
106	SA17	PSP*	Expansion Bus, Address Bit 17	---
107	SA16	PSP*	Expansion Bus, Address Bit 16	---
108	SA15	PSP*	Expansion Bus, Address Bit 15	---
109	SA14	PSP*	Expansion Bus, Address Bit 14	---
110	SA13	PSP*	Expansion Bus, Address Bit 13	---
111	SA12	PSP*	Expansion Bus, Address Bit 12	---
112	SA11	PSP*	Expansion Bus, Address Bit 11	---
113	SA10	PSP*	Expansion Bus, Address Bit 10	---
114	SA9	PSP*	Expansion Bus, Address Bit 9	---
115	SA8	PSP*	Expansion Bus, Address Bit 8	---
116	SA7	PSP*	Expansion Bus, Address Bit 7	---
117	SA6	PSP*	Expansion Bus, Address Bit 6	---
118	SA5	PSP*	Expansion Bus, Address Bit 5	---
119	SA4	PSP*	Expansion Bus, Address Bit 4	---
120	SD15	PSP*	Expansion Bus, Data Bit 15	✓
121	SD14	PSP*	Expansion Bus, Data Bit 14	✓
122	SD13	PSP*	Expansion Bus, Data Bit 13	✓
123	SD12	PSP*	Expansion Bus, Data Bit 12	✓
124	SD11	PSP*	Expansion Bus, Data Bit 11	✓
125	SD10	PSP*	Expansion Bus, Data Bit 10	✓
126	SD9	PSP*	Expansion Bus, Data Bit 9	✓
127	SD8	PSP*	Expansion Bus, Data Bit 8	✓
128	VCC	---	3.3 Volt Power Input	✓

* = Product Specific Pins

Table 6: Pinout QIL-128 socket – pin 66 to 128

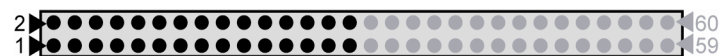
*** Please note:** Some pins are called "Product Specific Pins (PSP)". Other members of the *DIL/NetPC* family will differ with these pins from the *ADNP/9200*. All other pins will have the same primary functions.



3.5 Expansion Bus – J2 (1. Part)

Pin	Name	Function
1	VCC	3.3 Volt Power Input
2	GND	Ground
3	SD0	Data Bit 0
4	SD8	Data Bit 8
5	SD1	Data Bit 1
6	SD9	Data Bit 9
7	SD2	Data Bit 2
8	SD10	Data Bit 10
9	SD3	Data Bit 3
10	SD11	Data Bit 11
11	SD4	Data Bit 4
12	SD12	Data Bit 12
13	SD5	Data Bit 5
14	SD13	Data Bit 13
15	SD6	Data Bit 6
16	SD14	Data Bit 14
17	SD7	Data Bit 7
18	SD15	Data Bit 15
19	SA0	Address Bit 0
20	SA4	Address Bit 4
21	SA1	Address Bit 1
22	SA5	Address Bit 5
23	SA2	Address Bit 2
24	SA6	Address Bit 6
25	SA3	Address Bit 3
26	SA7	Address Bit 7
27	IOW	I/O Write Signal
28	SA8	Address Bit 8
29	IOR	I/O Read Signal
30	SA9	Address Bit 9

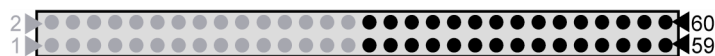
Table 7: Pinout expansion bus – pin 1 to pin 30



3.6 Expansion Bus – J2 (2. Part)

Pin	Name	Function
31	IOCHRDY	I/O Channel Ready
32	SA10	Address Bit 10
33	CS1	Programmable Chip Select Output 1
34	SA11	Address Bit 11
35	CS2	Programmable Chip Select Output 2
36	SA12	Address Bit 12
37	CS3	Programmable Chip Select Output 3
38	SA13	Address Bit 13
39	CS4	Programmable Chip Select Output 4
40	SA14	Address Bit 14
41	INT1	Programmable Interrupt Input 1
42	SA15	Address Bit 15
43	DDP	USB Device Port +
44	SA16	Address Bit 16
45	DDM	USB Device Port -
46	SA17	Address Bit 17
47	HDP	USB Host Port +
48	SA18	Address Bit 18
49	HDM	USB Host Port -
50	SA19	Address Bit 19
51	---	Reserved – Do not use
52	SA20	Address Bit 20
53	---	Reserved – Do not use
54	SA21	Address Bit 21
55	CLKOUT	Clock Output
56	SA22	Address Bit 22
57	---	Reserved – Do not use
58	SA23	Address Bit 23
59	RSTOUT	Reset Output (High Active)
60	RSTDRV	Reset Output (Low Active)

Table 8: Pinout expansion bus – pin 31 to pin 60



3.7 LCD Connector – J3

Pin	Name	Function
1	---	Reserved – Do not use
2	GND	Ground
3	VCC*	5 VDC Power Input
4	VO	Contrast Voltage over R27
5	IOW	I/O Write Signal
6	IOR	I/O Read Signal
7	CS2	Programmable Chip Select Output 2
8	SA2	Address Bit 2
9	RSTOUT	Reset Output
10	BUF_SD0	Buffered Data Bit 0
11	BUF_SD1	Buffered Data Bit 1
12	BUF_SD2	Buffered Data Bit 2
13	BUF_SD3	Buffered Data Bit 3
14	BUF_SD4	Buffered Data Bit 4
15	BUF_SD5	Buffered Data Bit 5
16	BUF_SD6	Buffered Data Bit 6
17	BUF_SD7	Buffered Data Bit 7
18	---	Reserved – Do not use

Table 9: Pinout LCD connector

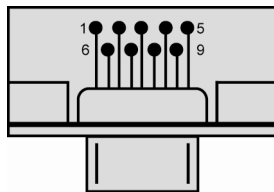


* **Please note:** The 5 VDC output is connected directly to pin 1 of J17 (power connector).

3.8 COM1 Connector – J4

Pin	Name	Function
1	DCD	COM1 Serial Port, DCD pin
2	RXD	COM1 Serial Port, RXD pin
3	TXD	COM1 Serial Port, TXD pin
4	DTR	COM1 Serial Port, DTR pin
5	GND	Ground
6	DSR	COM1 Serial Port, DSR pin
7	RTS	COM1 Serial Port, RTS pin
8	CTS	COM1 Serial Port, CTS pin
9	RI	COM1 Serial Port, RI pin

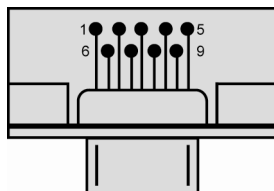
Table 10: Pinout COM1 connector



3.9 COM2 Connector – J5

Pin	Name	Function
1	---	---
2	RXD	COM2 Serial Port, RXD pin
3	TXD	COM2 Serial Port, TXD pin
4	---	---
5	GND	Ground
6	---	---
7	---	---
8	---	---
9	---	---

Table 11: Pinout COM2 connector



3.10 CompactFlash Socket – J7 (1. Part)

Pin	Name	Function
1	GND	Ground
2	D3	SD3
3	D4	SD4
4	D5	SD5
5	D6	SD6
6	D7	SD7
7	CE1	CS0*
8	A10	Ground
9	OE	MEMR*
10	A9	Ground
11	A8	Ground
12	A7	Ground
13	VCC	3.3 VDC Power Input
14	A6	Ground
15	A5	Ground
16	A4	Ground
17	A3	SA3
18	A2	SA2
19	A1	SA1
20	A0	SA0
21	D0	SD0
22	D1	SD1
23	D2	SD2
24	WP	CTRL3*
25	CD2	---

* depends on position of JP2

Table 12: Pinout CompactFlash socket – pin 1 to 25



3.11 CompactFlash Socket – J7 (2. Part)

Pin	Name	Function
26	CD1#	---
27	D11	SD11
28	D12	SD12
29	D13	SD13
30	D14	SD14
31	D15	SD15
32	CE2	CS1*
33	VS1	---
34	IOR	IDE_1
35	IOW	IDE_0
36	WE	VCC3
37	RDY/BSY	CTRL4 IRQ
38	VCC	3.3 VDC Power Input
39	CSEL	IDE_10
40	VS2	---
41	RESET	IDE_5
42	WAIT	CTRL2 IOCHRDY*
43	INPACK	---
44	REG	VCC3
45	BVD2	LED*
46	BVD1	---
47	D8	SD8
48	D9	SD9
49	D10	SD10
50	GND	Ground

* depends on position of JP2

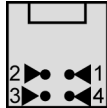
Table 13: Pinout CompactFlash socket – pin 26 to 50



3.12 USB Device Port – J8

Pin	Name	Function
1	VCC	Power Input
2	DATA-	USB Device Port -
3	DATA+	USB Device Port +
4	GND	Ground

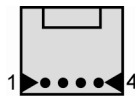
Table 14: Pinout USB device port



3.13 USB Host Port – J9

Pin	Name	Function
1	5 VDC	5 VDC Output*
2	DATA-	USB Host Port -
3	DATA+	USB Host Port +
4	GND	Ground

Table 15: Pinout USB host port

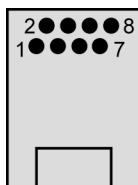


* **Please note:** Jumper JP5 (USB 5 VDC) enables the 5 VDC output of this pin.

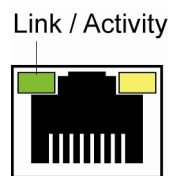
3.14 10/100 Mbps Ethernet Connector 1 – J10

Pin	Name	Function
1	LAN1.TX+	10/100 Mbps LAN 1, TX+ Pin
2	LAN1.TX-	10/100 Mbps LAN 1, TX- Pin
3	LAN1.RX+	10/100 Mbps LAN 1, RX+ Pin
4	---	Not Connected
5	---	Not Connected
6	LAN1.RX-	10/100 Mbps LAN 1, RX- Pin
7	---	Not Connected
8	---	Not Connected

Table 16: Pinout 10/100 Mbps Ethernet connector 1



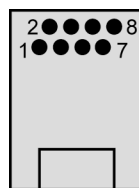
The Ethernet connector owns one green LED in the upper left which shows if a LAN link is established and if there is LAN activity. The yellow LED is not connected.



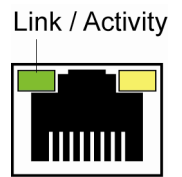
3.15 10/100 Mbps Ethernet Connector 2 – J11

Pin	Name	Function
1	LAN2.TX+	10/100 Mbps LAN 2, TX+ Pin
2	LAN2.TX-	10/100 Mbps LAN 2, TX- Pin
3	LAN2.RX+	10/100 Mbps LAN 2, RX+ Pin
4	---	Not Connected
5	---	Not Connected
6	LAN2.RX-	10/100 Mbps LAN 2, RX- Pin
7	---	Not Connected
8	---	Not Connected

Table 17: Pinout 10/100 Mbps Ethernet connector 2



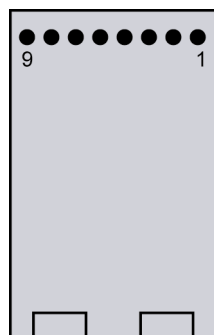
The Ethernet connector owns one green LED in the upper left which shows if a LAN link is established and if there is LAN activity. The yellow LED is not connected.



3.16 100 Mbps 100BASE-FX Ethernet Connector 2 – J12

Pin	Name	Function
1	GND	Ground
2	LAN2.RX+	100 Mbps LAN 2, RX+ Pin
3	LAN2.RX-	100 Mbps LAN 2, RX- Pin
4	LAN2.SD	100 Mbps LAN 2, Signal Detect Pin
5	VCC	3.3 VDC Power Input
6	VCC	3.3 VDC Power Input
7	LAN2.TX-	100 Mbps LAN 2, TX- Pin
8	LAN2.TX+	100 Mbps LAN 2, TX+ Pin
9	GND	Ground

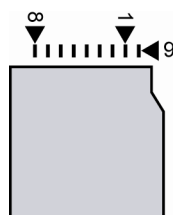
Table 18: Pinout 100 Mbps 100BASE-FX Ethernet connector 2



3.17 SD/MMC Socket – J13

Pin	Name	Function
1	MCDA3'	Multimedia Card A Data 3
2	MCCDA'	Multimedia Card A Command
3	GND	Ground
4	VCC	3.3 VDC
5	MCCK'	Multimedia Card Clock
6	GND	Ground
7	MCDA0'	Multimedia Card A Data 0
8	MCDA1'	Multimedia Card A Data 1
9	MCDA2'	Multimedia Card A Data 2

Table 19: Pinout SD/MMC socket

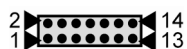


Please note: The pins 1, 2, 5, 7, 8 and 9 go over jumper JP3.

3.18 SPI Connector – J14

Pin	Name	Function
1	VCC	3.3 VDC
2	GND	Ground
3	MOSI	Master Out Slave In (SPI Data)
4	GND	Ground
5	MISO	Master In Slave Out (SPI Data)
6	GND	Ground
7	SPICLK	SPI Clock
8	GND	Ground
9	SPICSS0	SPI Chip Select
10	GND	Ground
11	---	Not Connected
12	GND	Ground
13	---	Not Connected
14	VCC	3.3 VDC Power Input

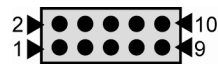
Table 20: Pinout SPI connector



3.19 JTAG Port – J15

Pin	Name	Function
1	TDI	Test Data In
2	TDO	Test Data Out
3	TMS	Test Mode Select
4	GND	Ground
5	TRST#	Test Reset
6	TCK	Test Clock
7	VCC3	3.3 VDC Power Input
8	---	Not connected
9	WDDIS#	Watchdog Disable
10	GND	Ground

Table 21: Pinout JTAG port

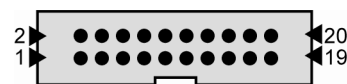


With this connector you have access to the JTAG interface of the DIL/NetPC mounted on the DNP/EVA11. To use this port a special adapter and software is required. Please contact SSV for further information.

3.20 Multi-ICE Connector – J16

Pin	Name	Function
1	VCC	3.3 VDC Power Input
2	---	Not connected
3	TRST#	Test Reset
4	---	Not connected
5	TDI	Test Data In
6	---	Not connected
7	TMS	Test Mode Select
8	GND	Ground
9	TCK	Test Clock
10	GND	Ground
11	---	Not connected
12	---	Not connected
13	TDO	Test Data Out
14	---	Not connected
15	RSTOUT	Reset Output
16	---	Not connected
17	---	Not connected
18	---	Not connected
19	---	Not connected
20	---	Not connected

Table 22: Pinout Multi-ICE connector

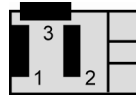


Please note: If you use Multi-ICE it is necessary to disable the Watchdog by placing a jumper cap on pin 9 and 10 of the JTAG port J15!

3.21 Power Connector – J17

Pin	Name	Function
1	5 VDC	Power In (max. 5.5 VDC)
2	GND	Ground
3	GND	Ground

Table 23: Pinout power connector



CAUTION: Providing the DNP/EVA11 with a voltage higher than the regular 5 VDC $\pm 10\%$ could cause damaged board components!

3.22 RCM Jumper – JP1

The **Remote Console Mode (RCM)** realizes some basic operating modes such as a boot loader or a ROM-monitor program.

Note: The default setting of the RCM jumper is set. Only if the RCM jumper is set you will be able to boot Linux on the DIL/NetPC.

To disable RCM on the DIL/NetPC remove the jumper cap from JP1.

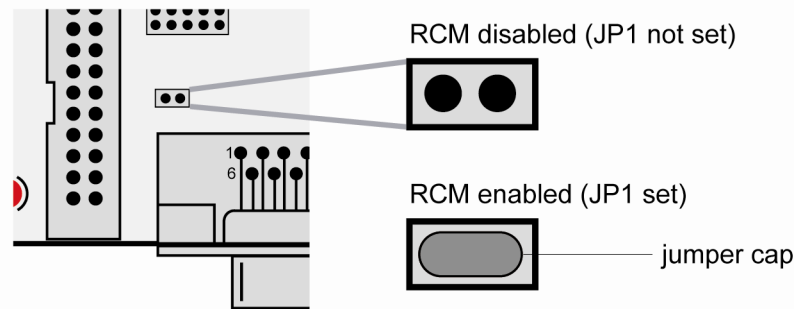


Figure 5: Position of RCM jumper

3.23 CF Mode Jumper – JP2

The CompactFlash socket can run in three different operating modes:

- Mode 1: MEM mode with 8 bit
- Mode 2: MEM mode with 16 bit
- Mode 3: IDE mode with 16 bit

Note: The default setting of the CF mode jumper is mode 1.

To change the operating mode of the CompactFlash socket place the jumper caps like shown in the following figure.

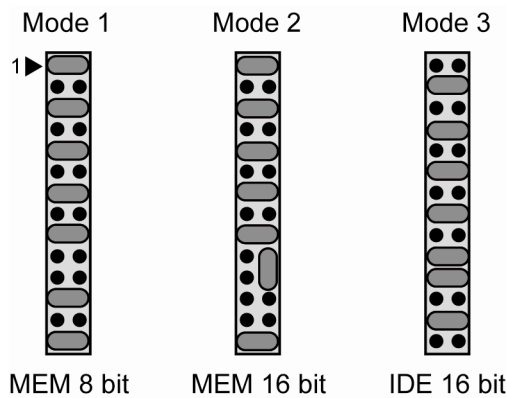


Figure 6: Configuration of CF mode jumper

3.24 MMC Mode Jumper – JP3

Pin	Name	Function
1	MCDA3	Multimedia Card A Data 3
2	MCDA3'	...to SD/MMC Socket Pin 1
3	MCCDA	Multimedia Card A Command
4	MCCDA'	...to SD/MMC Socket Pin 2
5	MCCK	Multimedia Card Clock
6	MCCK'	...to SD/MMC Socket Pin 5
7	MCDA0	Multimedia Card A Data 0
8	MCDA0'	...to SD/MMC Socket Pin 7
9	MCDA1	Multimedia Card A Data 1
10	MCDA1'	...to SD/MMC Socket Pin 8
11	MCDA2	Multimedia Card A Data 2
12	MCDA2'	...to SD/MMC Socket Pin 9

Table 24: Pinout MMC mode jumper

Note: The default setting of JP3 is set (MMC enabled):

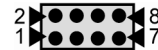


3.25 SPI Mode Jumper – JP4

Pin	Name	Function
1	MOSI	Master Out Slave In (SPI Data Output)
2	MCCDA	Multimedia Card A Command
3	MISO	Master In Slave Out (SPI Data Input)
4	MCDA0	Multimedia Card A Data 0
5	SPICLK	SPI Clock
6	MCCK	Multimedia Card Clock
7	SPICSS0	SPI Chip Select CS0
8	MCDA3	Multimedia Card A Data 3

Table 25: Pinout SPI mode jumper

The default setting of JP4 is not set (SPI disabled):



Please note: Do not set JP3 (MMC mode) and JP4 (SPI mode) at the same time. This could lead to errors.

3.26 USB VCC Jumper – JP5

Pin	Name	Function
1	VCC	3.3 VDC
2	ENA	USB 5 VDC Output Enabled

Table 26: Pinout USB mode jumper

Note: The default setting of JP5 is set (USB 5 VDC enabled):



If this jumper is set the 5 VDC output of the USB host port J9 is enabled. If this jumper is not set there is no voltage on the USB host port J9 available.

4 MECHANICAL DIMENSIONS

All length dimensions have a tolerance of 0.5 mm. The drillings are suitable for M3 screws.

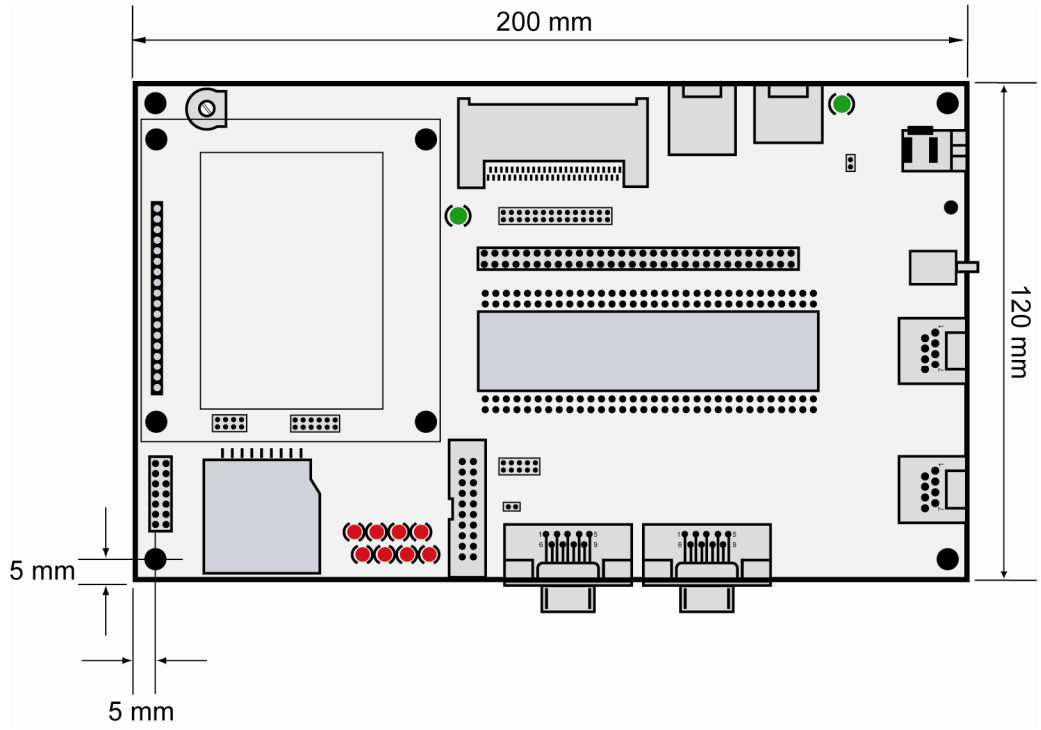


Figure 7: Mechanical dimensions of DNP/EVA11

The drillings of the LCD area are suitable for M2.5 screws.

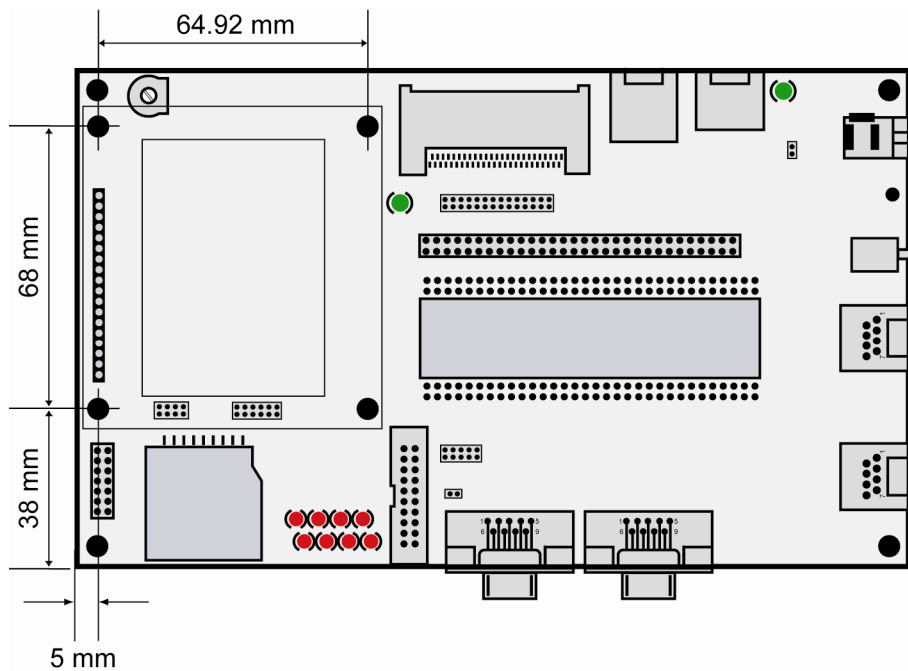


Figure 8: Mechanical dimensions of LCD area

5 HELPFUL LITERATURE

- Hardware Reference DIL/NetPC ADNP/9200

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