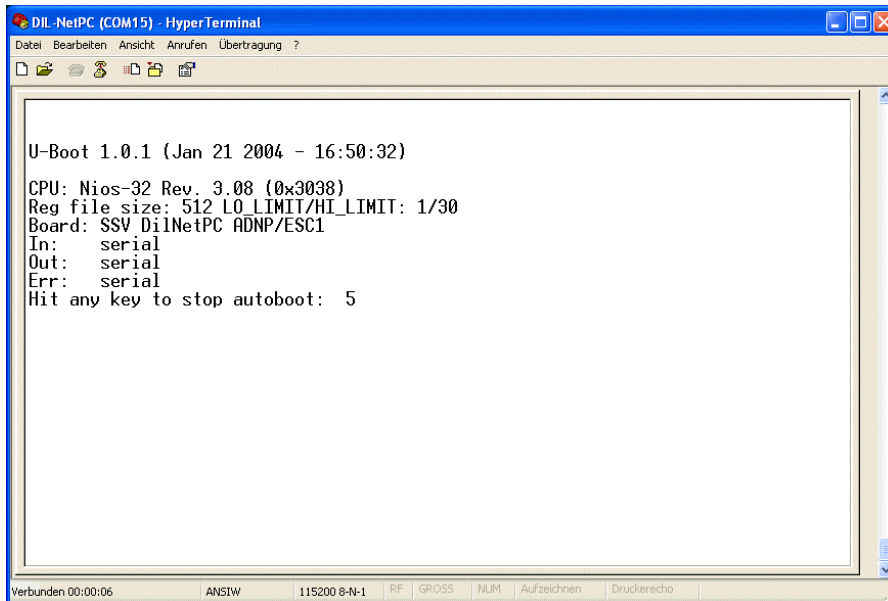


## How to understand the ADNP/ESC1 Boot Process

Direct after power-up the DIL/NetPC ADNP/ESC1 starts a automatic boot process from the on-board flash memory chip. This process consist of two steps.

- **1. Step:** The ADNP/ESC1 runs the U-Boot boot loader program. This software shows a wait message over the serial link. It is possible to interrupt the boot process. Just hit a key of your terminal emulation program.

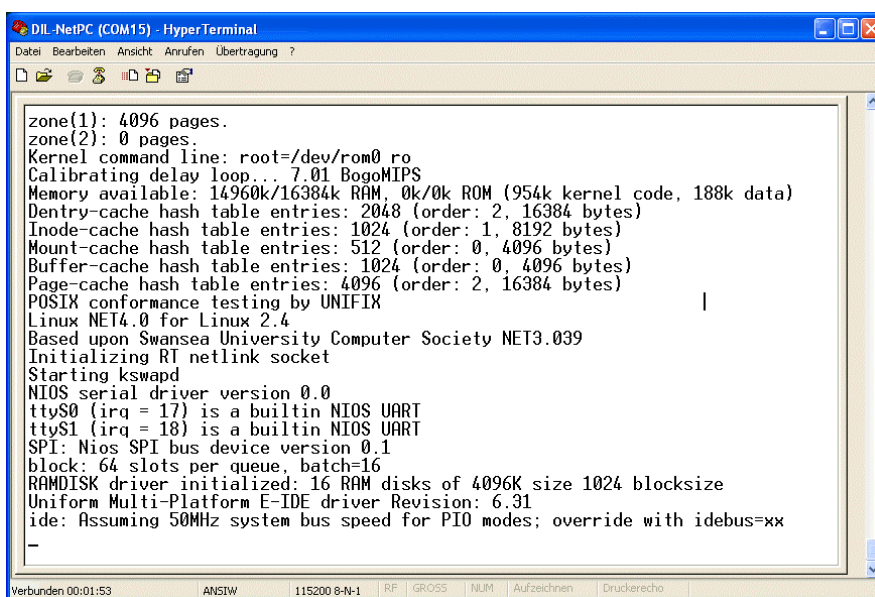


```

DIL-NetPC (COM15) - HyperTerminal
Datei Bearbeiten Ansicht Anrufen Übertragung ?
U-Boot 1.0.1 (Jan 21 2004 - 16:50:32)
CPU: Nios-32 Rev. 3.08 (0x3038)
Reg file size: 512 LO_LIMIT/HI_LIMIT: 1/30
Board: SSV DILNetPC ADNP/ESC1
In: serial
Out: serial
Err: serial
Hit any key to stop autoboot: 5
Verbunden 00:00:06 ANSIW 115200 8-N-1 RF GROSS NJM Aufzeichnen Druckererecho

```

- **2. Step:** Without interruption the U-Boot boot loader starts a Linux O/S image after the wait period from the ADNP/ESC1 flash memory.



```

DIL-NetPC (COM15) - HyperTerminal
Datei Bearbeiten Ansicht Anrufen Übertragung ?
zone(1): 4096 pages.
zone(2): 0 pages.
Kernel command line: root=/dev/rom0 ro
Calibrating delay loop... 7.01 BogoMIPS
Memory available: 14960k/16384k RAM, 0k/0k ROM (954k kernel code, 188k data)
Dentry-cache hash table entries: 2048 (order: 2, 16384 bytes)
Inode-cache hash table entries: 1024 (order: 1, 8192 bytes)
Mount-cache hash table entries: 512 (order: 0, 4096 bytes)
Buffer-cache hash table entries: 1024 (order: 0, 4096 bytes)
Page-cache hash table entries: 4096 (order: 2, 16384 bytes)
POSIX conformance testing by UNIFIX
Linux NET4.0 for Linux 2.4
Based upon Swansea University Computer Society NET3.039
Initializing RT netlink socket
Starting kswapd
NIOs serial driver version 0.0
ttyS0 (irq = 17) is a builtin NIOs UART
ttyS1 (irq = 18) is a builtin NIOs UART
SPI: Nios SPI bus device version 0.1
block: 64 slots per queue, batch=16
RAMDISK driver initialized: 16 RAM disks of 4096K size 1024 blocksize
Uniform Multi-Platform E-IDE driver Revision: 6.31
ide: Assuming 50MHz system bus speed for PIO modes; override with idebus=xx
-
Verbunden 00:01:53 ANSIW 115200 8-N-1 RF GROSS NJM Aufzeichnen Druckererecho

```

That is all.